

IN THE CLAIMS:

Please amend claims 1, 2, and 3, and cancel claims 4-18 without prejudice.

1. (Currently Amended) A semiconductor integrated circuit device comprising:
a semiconductor substrate defining a plurality of rows, each row including areas
for a sequence of cells;

a plurality of active regions disposed in each of said rows constituting
semiconductor elements of associated cells;

at least one [[a]] wiring region of a stripe shape elongated along a direction of a
row, defined on said semiconductor substrate outside of said active regions in each
row, and including wirings belonging to the associated cells, each wiring region having
height in a direction crossing the row direction, the at least one wiring region having a
locally different varying height.

2. (Currently Amended) The semiconductor integrated circuit device according to
claim 1, wherein [[said]] wiring regions of opposing cells in opposing rows have mutually
fitting shapes.

3. (Currently Amended) The semiconductor integrated circuit device according to
claim 1, wherein the wirings include ~~a wiring made of~~ a connection of different wiring
layers.

- 4-18. (Cancelled)